

⊙	7/0-2
⊙	53/0-2
⊙	24/0-2
⊙	32/0-2

Drng. No. Z 52955-900A
Sheet 1 of 2, Iss. 4

A0 : Overall connection diagram

FOR RF TRAY AA0 SEE DRG. Z4400-4865

Fig. 1

Mar. 86 (Am. 1)

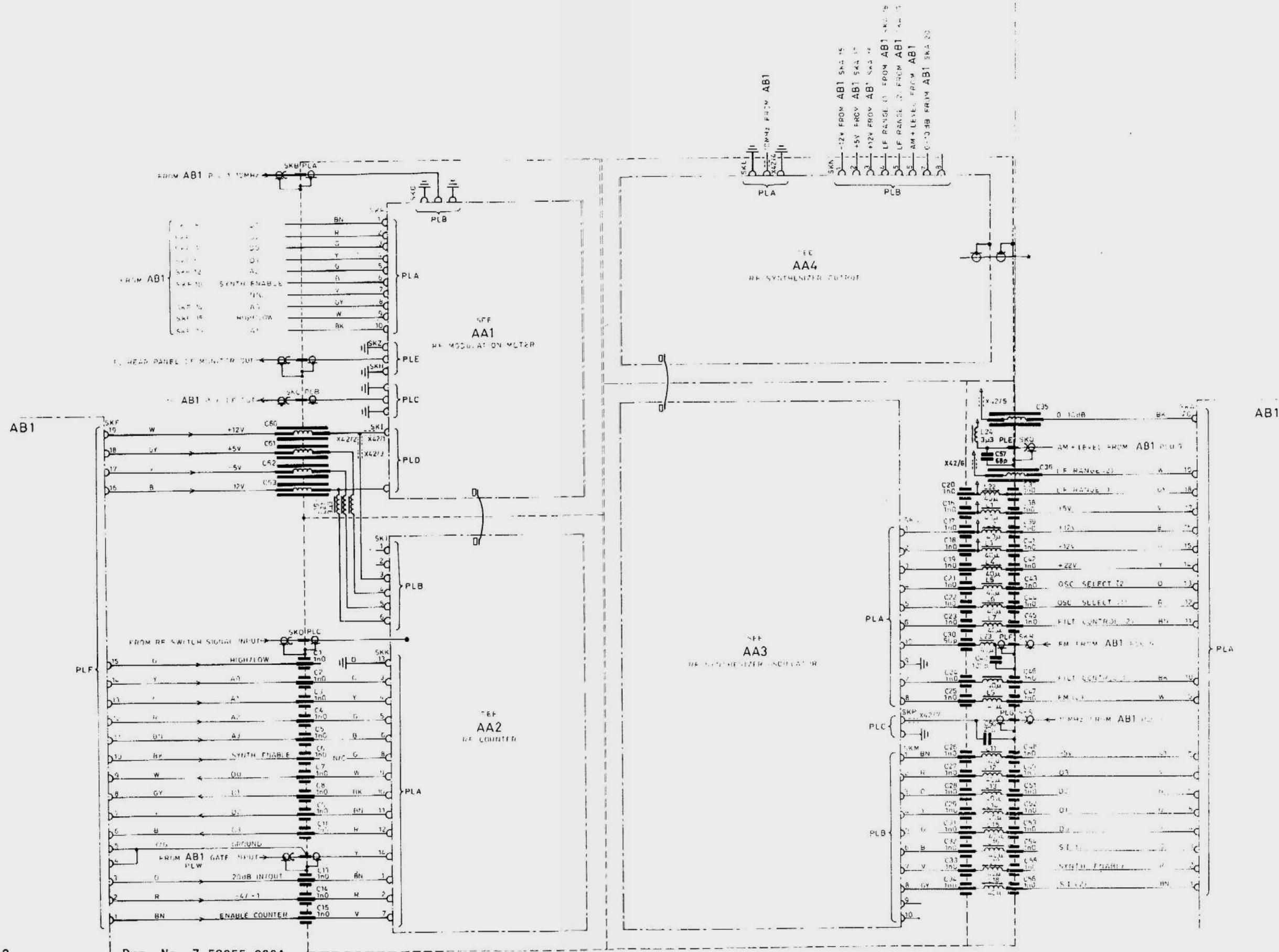
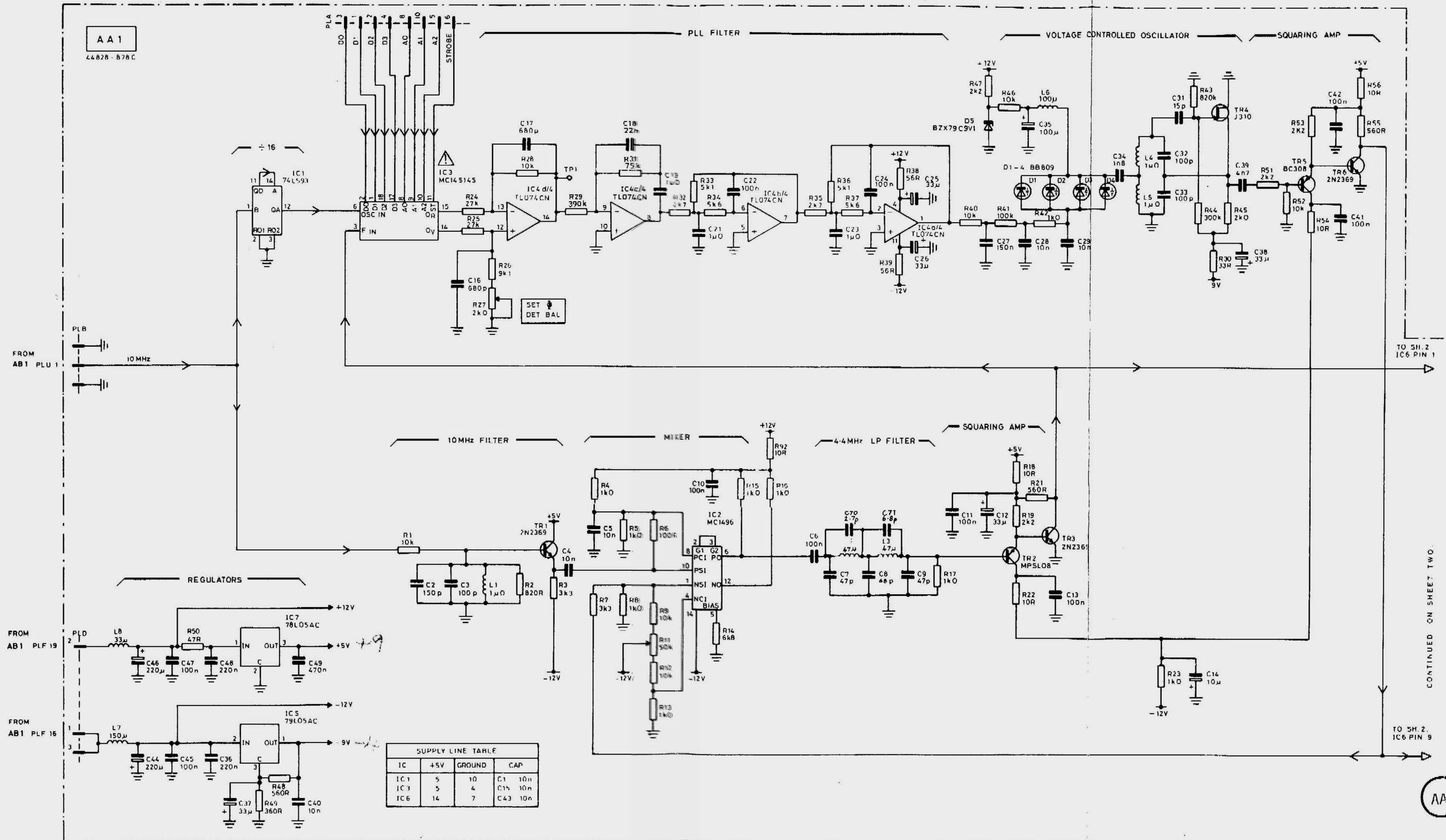


Fig. 2
Aug. 86 (Am. 2)

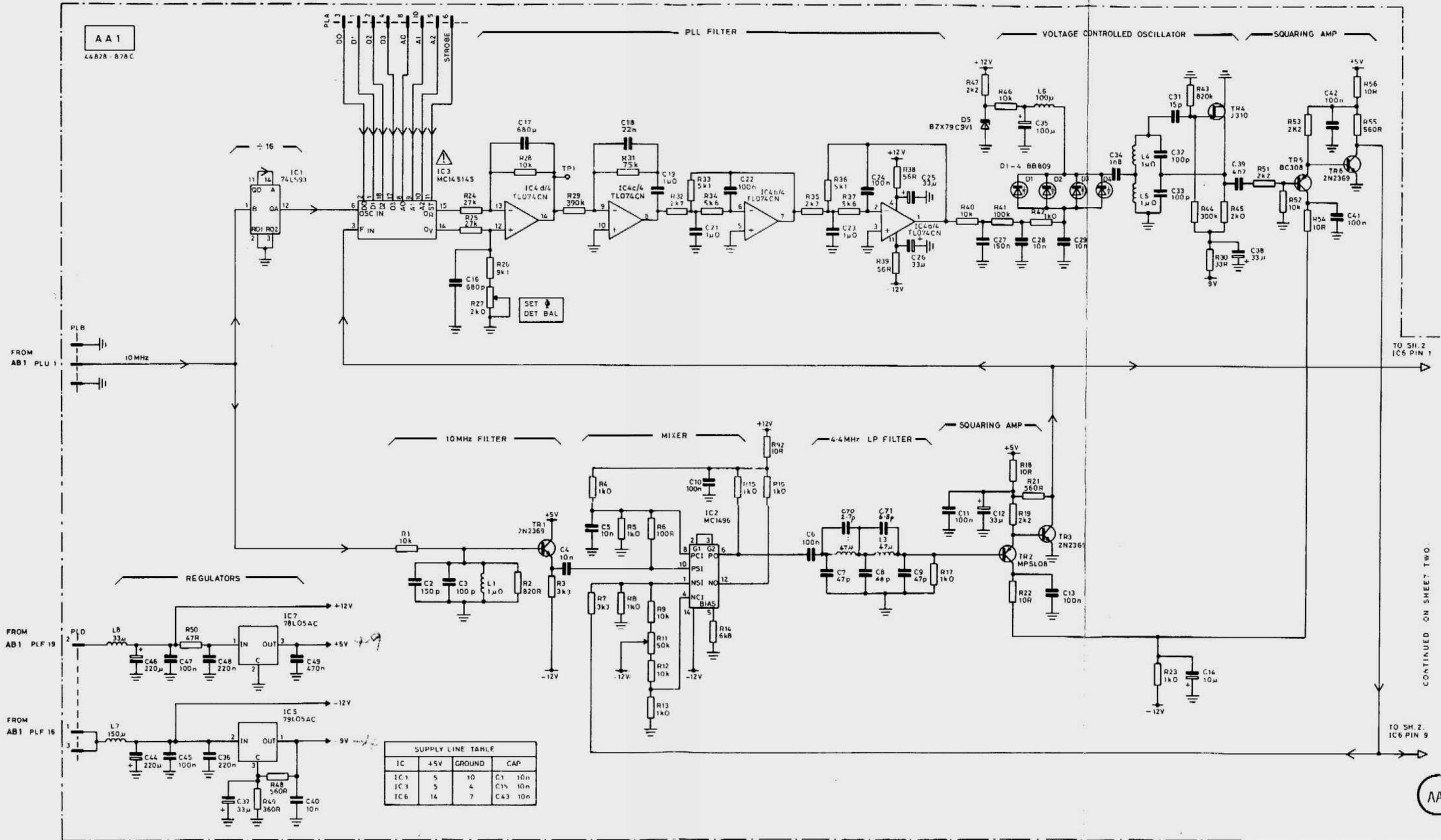
Drg. No. Z 52955-900A
Sh. 2 of 2, Iss. 7

AA0 : RF tray



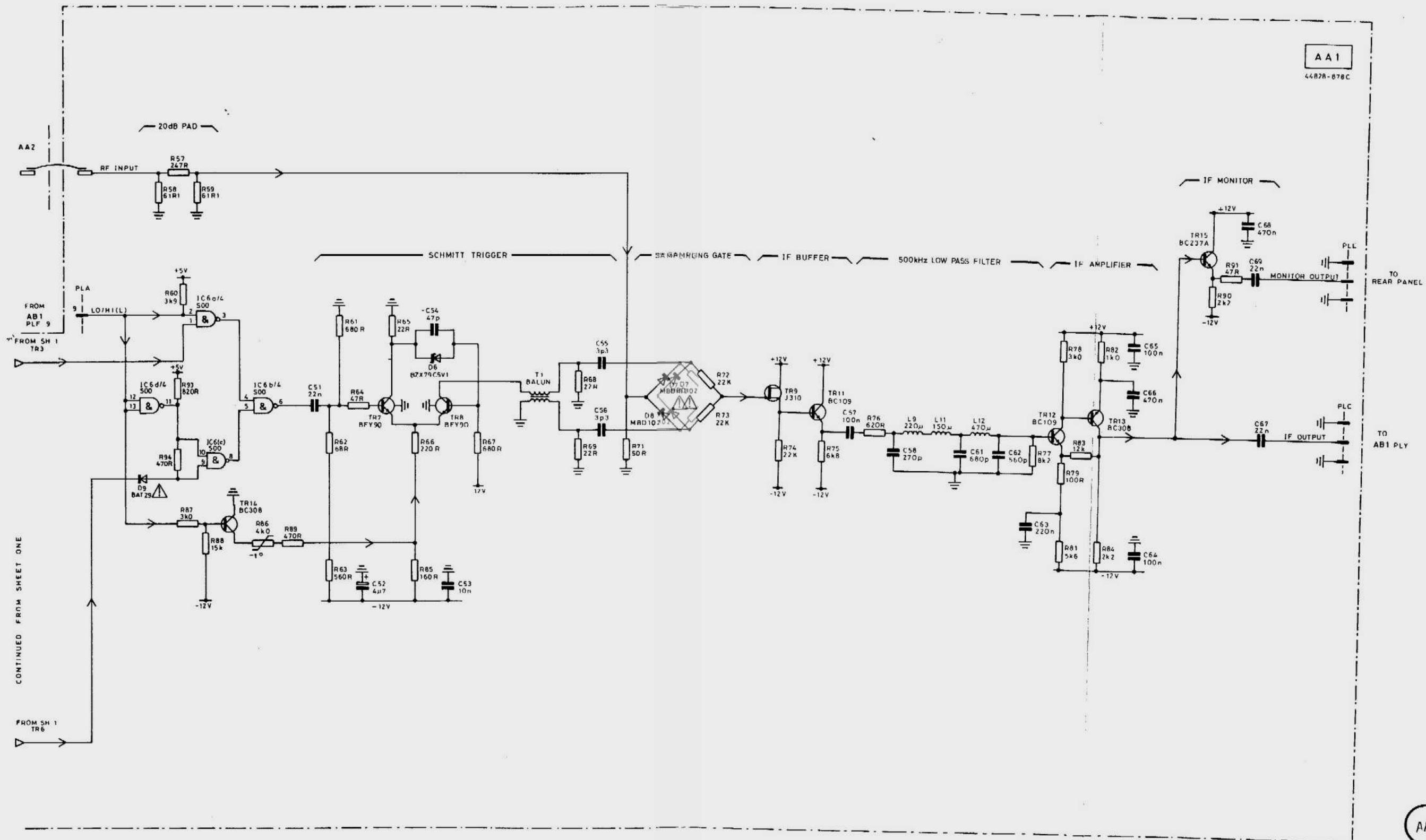
Drg. No. Z 44828-878C
Sht. 1 of 2, Iss. 7

AA1 : RF modulation meter (oscillator and p.l.l.)



Drg. No. Z 44828-878C
Sht. 1 of 2, Iss. 7

AA1 : RF modulation meter (oscillator and p.l.l.)

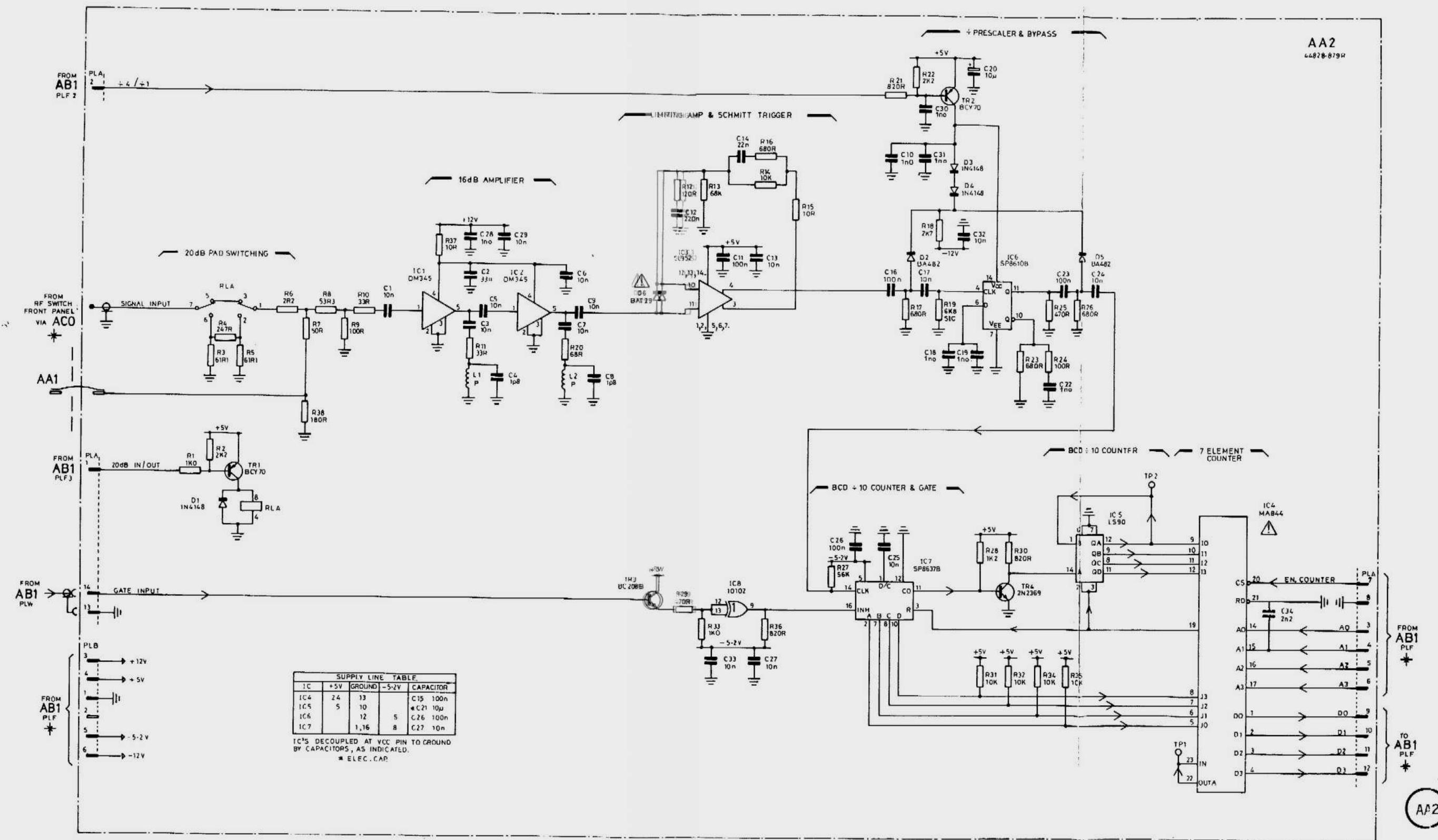


Drg. No. Z 44828-878C
Sh. 2 of 2, Iss. 4

AA1 : RF modulation meter (mixer)

Fig. 4

Mar. 86 (Am. 1)



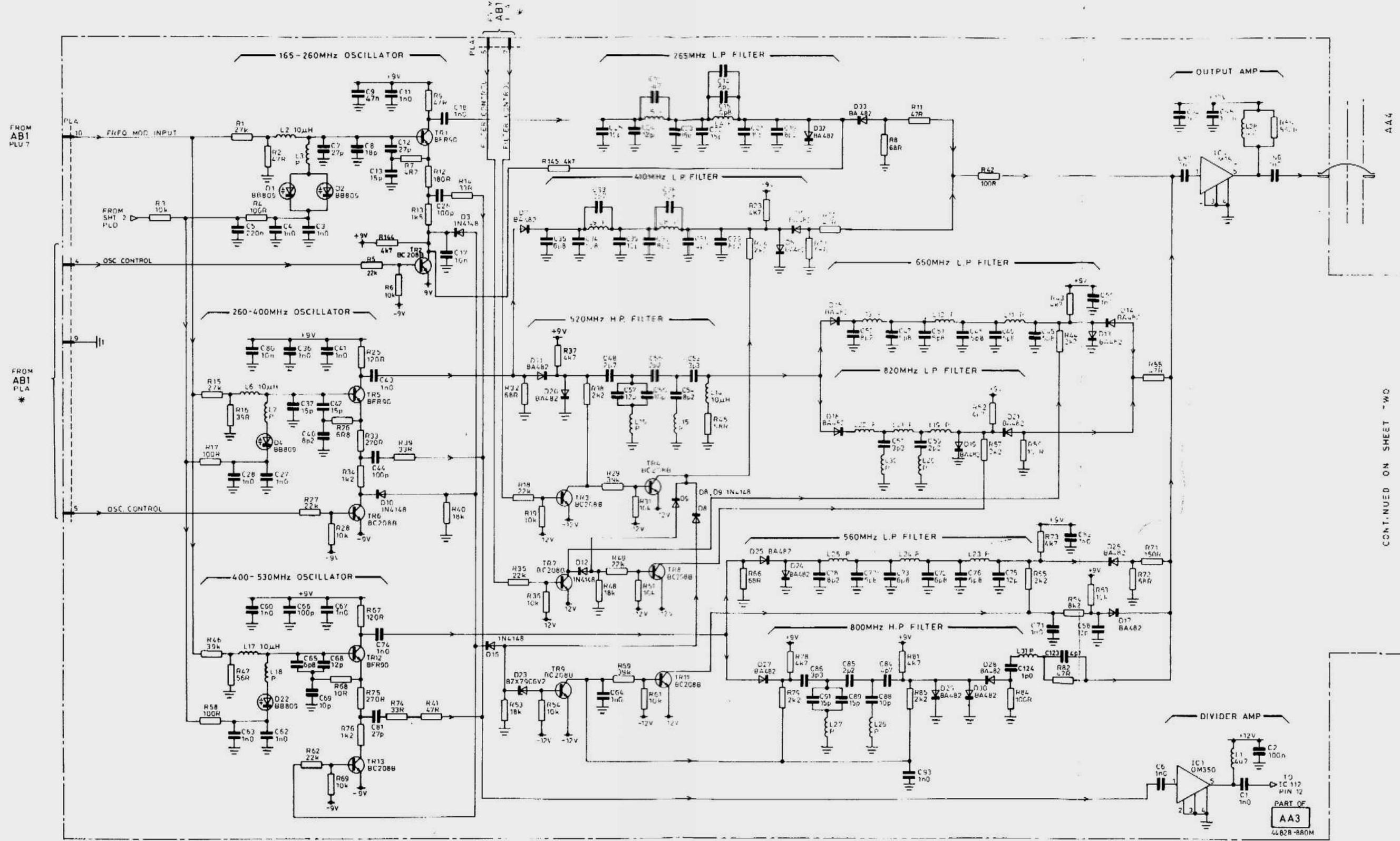
* SEE CIRCUIT AA0 FOR CONNECTIONS DETAILS

Drg. No. Z 44828-879R
Sh. 1 of 1, Iss. 4

Fig. 5
Jan. 87 (Am. 3)

AA2 : RF counter

Fig. 5
Chap. 7
Page 11



Drg. No. Z 44828-880M
Sht. 1 of 2, Iss. 5

* SEE CIRCUIT AAO FOR CONNECTIONS DETAILS

Fig. 6
Nov. 87 (Am. 5)

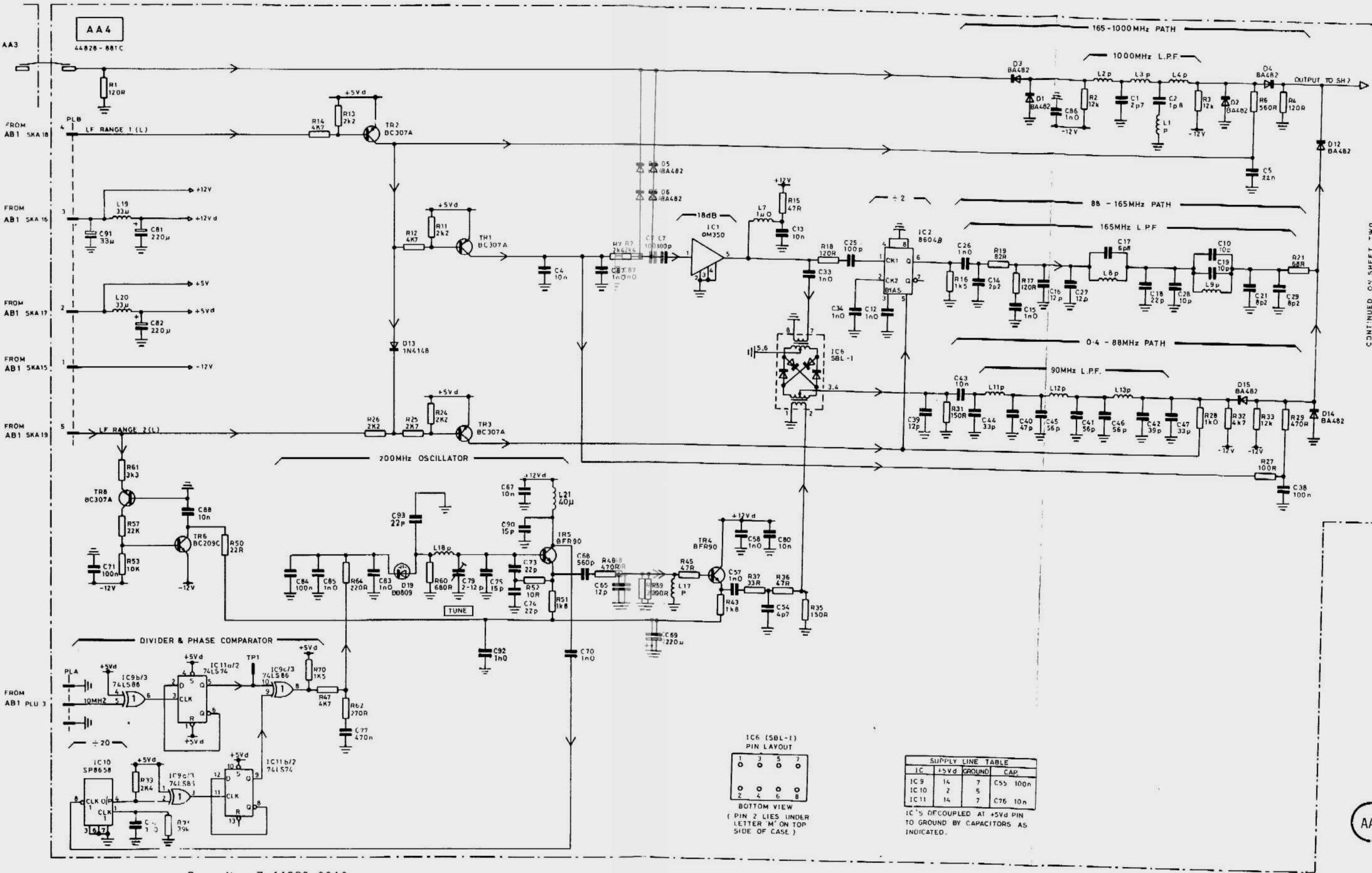
AA3 : RF synthesizer & oscillator (filters)

Fig. 6
Chap. 7
Page 13/14

AA4

CONTINUED ON SHEET TWO

AA3



CONTINUED ON SHEET TWO

AA4

Drg. No. Z 44828-881C
Sht. 1 of 2, Iss. 10

AA4 : Low frequency synthesizer & output amp. (mixer)

Fig. 8
Nov. 87 (Am. 5)

Fig. 8
Chap. 7
Page 17/18

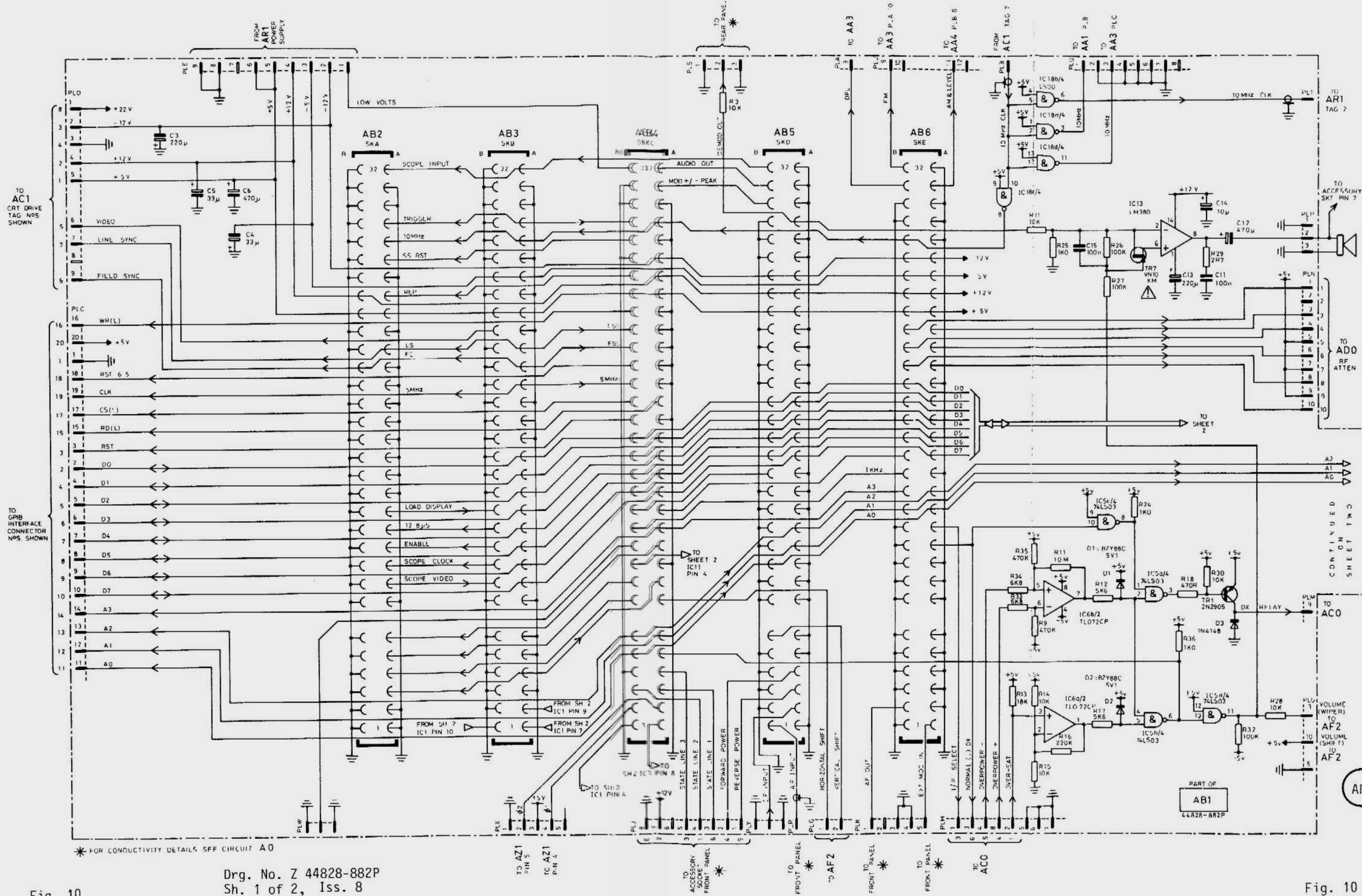


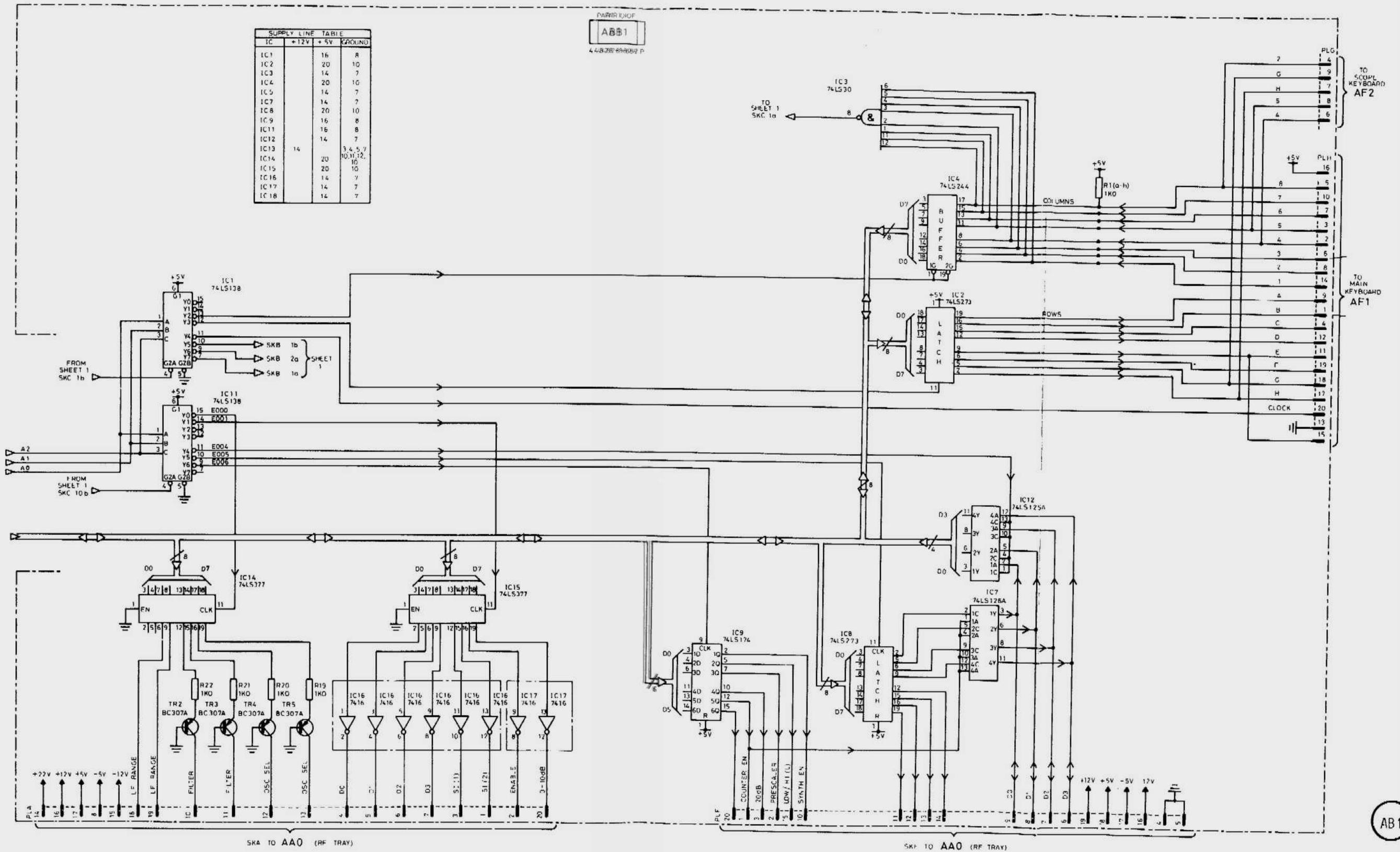
Fig. 10

Drg. No. Z 44828-882P
 Sh. 1 of 2, Iss. 8

Mar. 86 (Am. 1)

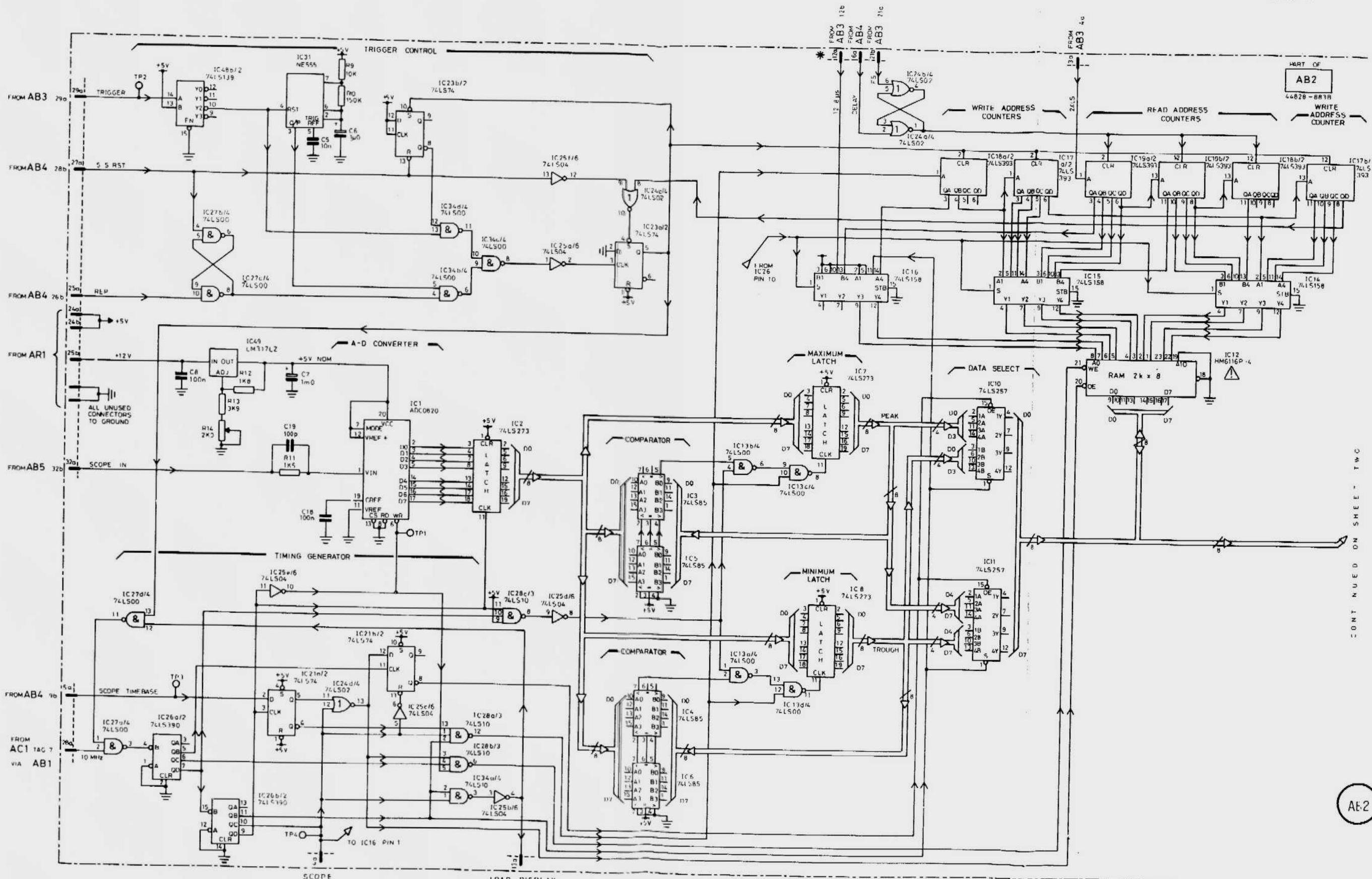
AB1 : Motherboard (audio amp. & overload protection)

Fig. 10
 Chap. 7
 Page 21/22



Drg. No. Z 44828-882P
Sh. 2 of 2, Iss. 6

AB1 : Motherboard (keyboard interface)



CONTINUED ON SHEET TWO

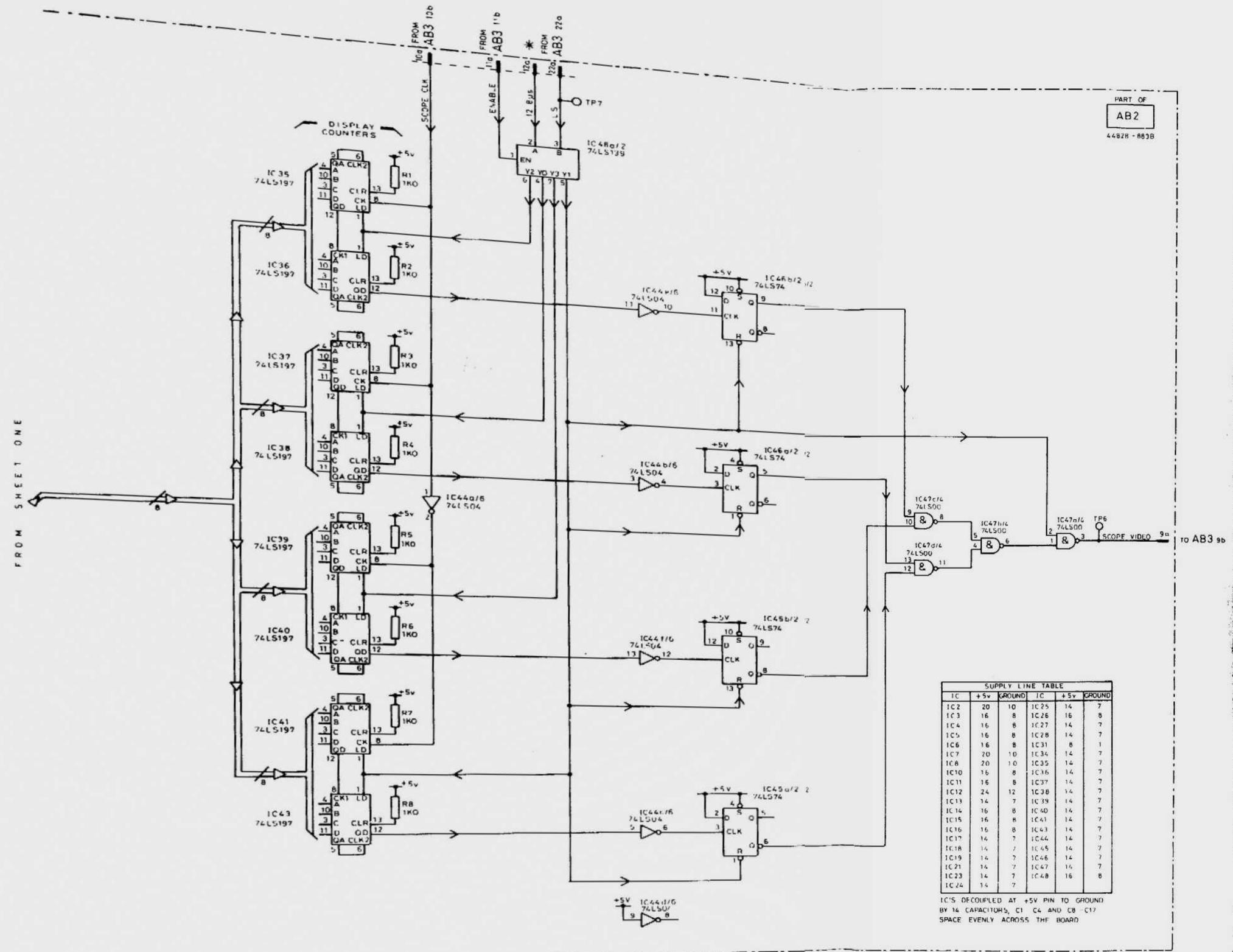
AE-2

Fig. 12 Drg. No. Z 44828-883B
Sh. 1 of 2, Iss. 8
Jan. 87 (Am. 3)

SCOPE TIMEBASE
To AB4 8b

LOAD DISPLAY COUNTERS
To AB3 13b

AB2 : Digital scope (A-D and timing)



SUPPLY LINE TABLE

IC	+5v	GROUND	IC	+5v	GROUND
IC2	20	10	IC25	14	7
IC3	16	8	IC26	16	8
IC4	16	8	IC27	14	7
IC5	16	8	IC28	14	7
IC6	16	8	IC31	8	1
IC7	20	10	IC34	14	7
IC8	20	10	IC35	14	7
IC10	16	8	IC36	14	7
IC11	16	8	IC37	14	7
IC12	24	12	IC38	14	7
IC13	14	7	IC39	14	7
IC14	16	8	IC40	14	7
IC15	16	8	IC41	14	7
IC16	16	8	IC43	14	7
IC17	14	7	IC44	14	7
IC18	14	7	IC45	14	7
IC19	14	7	IC46	14	7
IC21	14	7	IC47	14	7
IC23	14	7	IC48	16	8
IC24	14	7			

IC'S DECOUPLED AT +5V PIN TO GROUND BY 14 CAPACITORS, C1 C4 AND C8 C17 SPACE EVENLY ACROSS THE BOARD

Drq. No. Z 44828-883B
Sh. 2 of 2, Iss. 1

AB2 :: Digital scope (output)

Fig. 13
June 85

Fig. 13
Chap. 7
Page 27

AB2

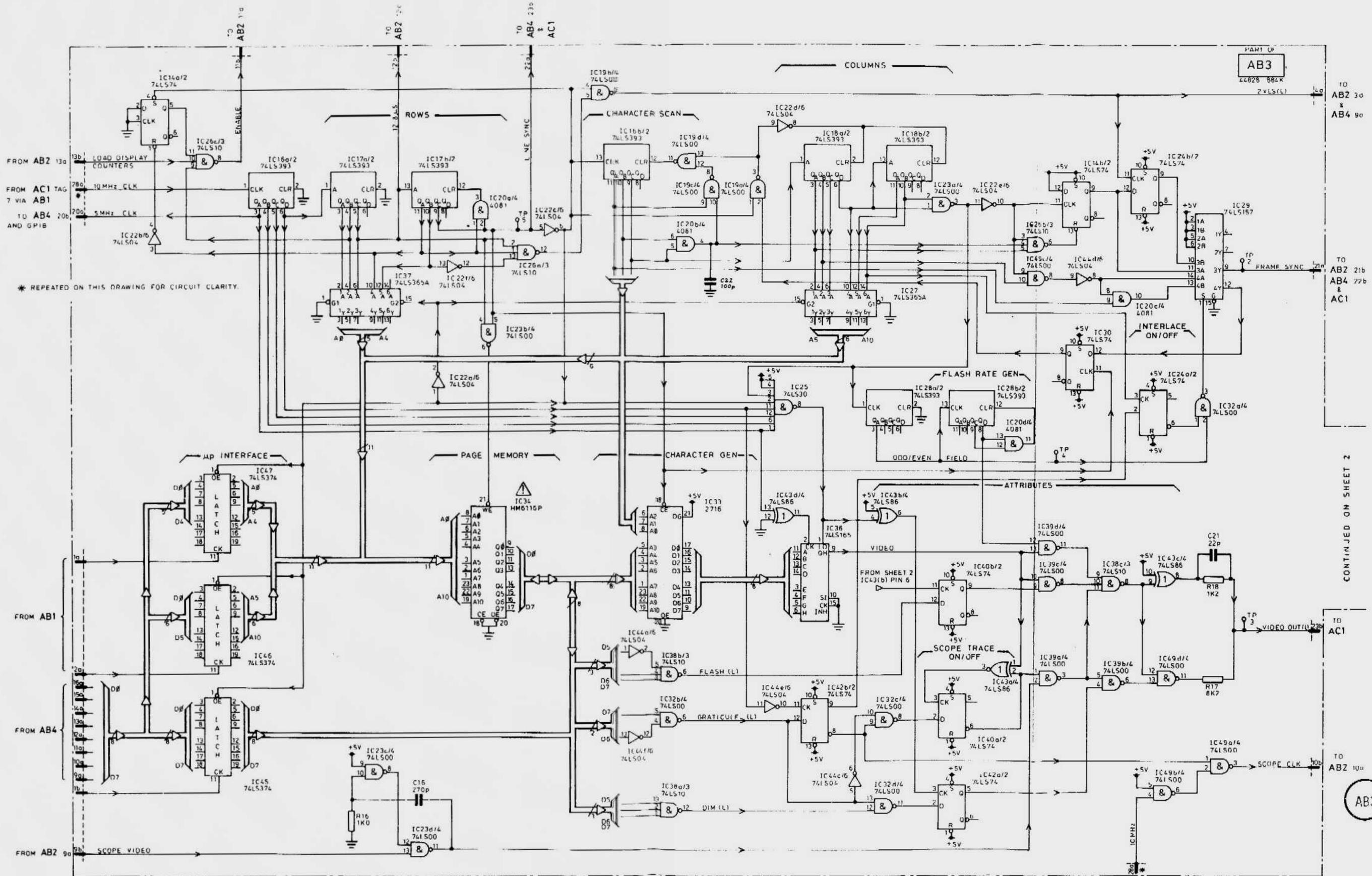
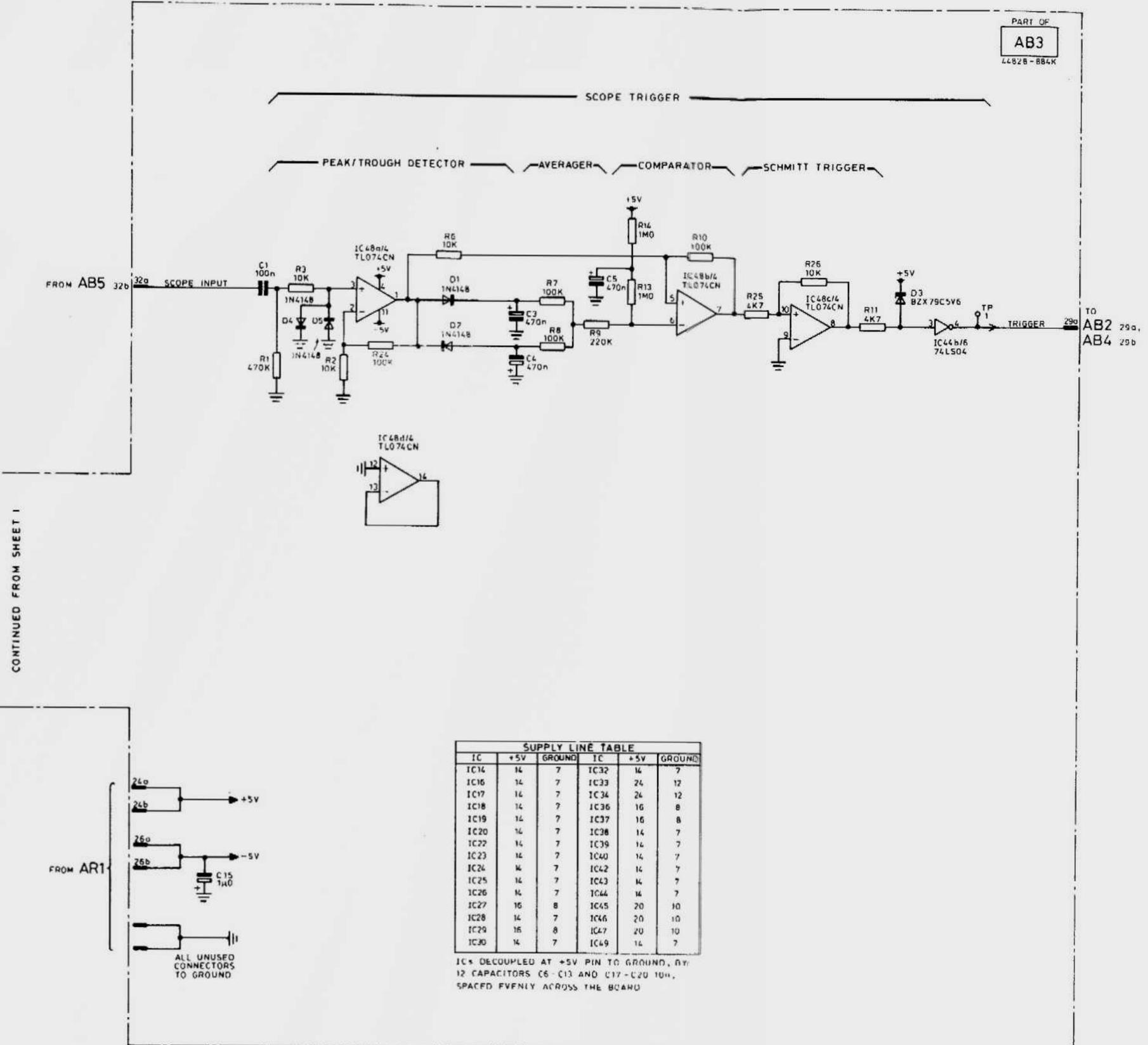


Fig. 14
Nov. 87 (Am. 5)

Drg. No. Z 44828-884K
Sht. 1 of 2, Iss. 7

AB3 : VDU board (character generator)

Fig. 14
Chap. 7
Page 29/30



CONTINUED FROM SHEET 1

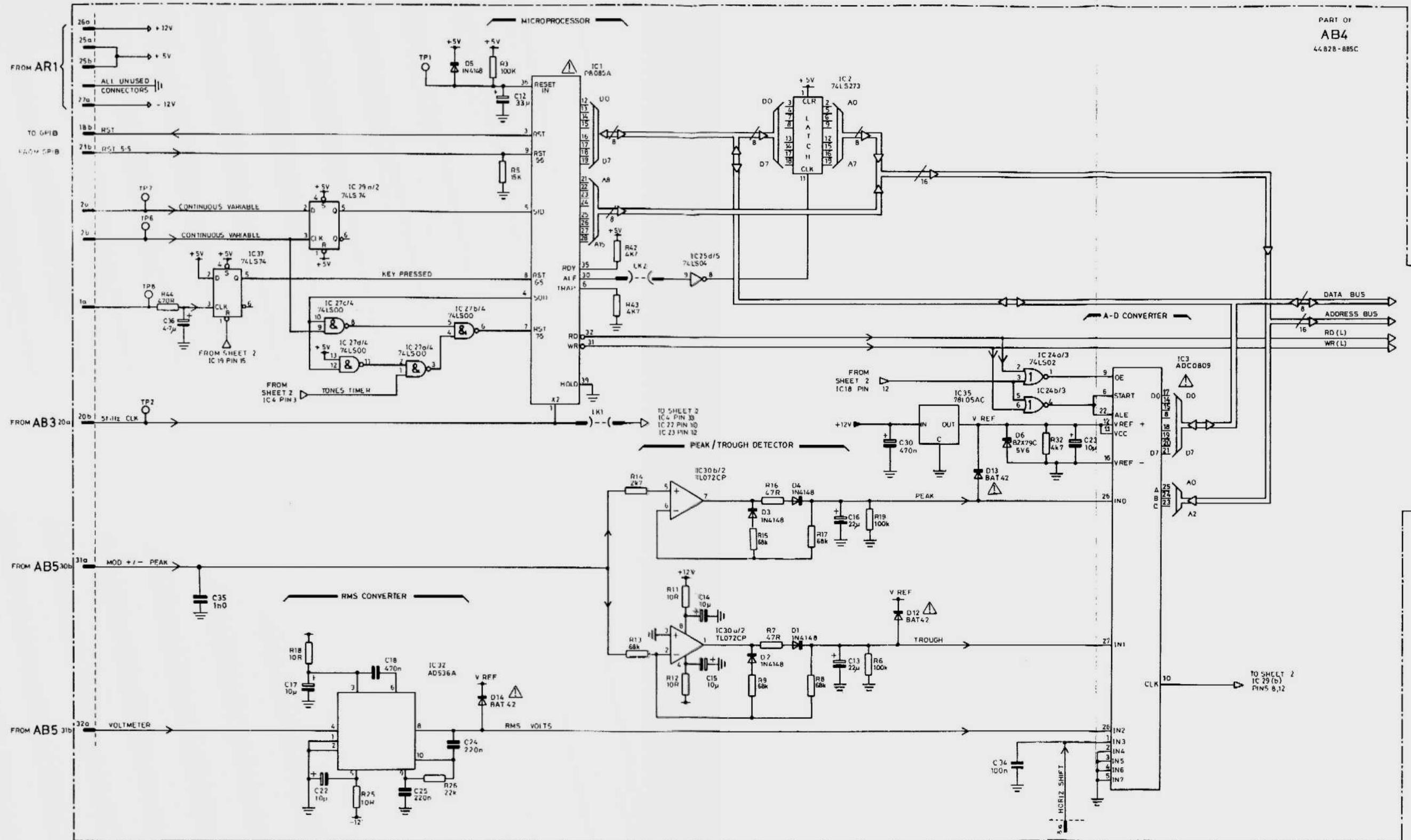
Drg. No. Z 44828-884K
Sh. 2 of 2, Iss. 5

Fig. 15
June 85

AB3 : VDU board (scope trigger)

Fig. 15
Chap. 7
Page 31

AB3



Drg. No. Z 44828-885A
Sh. 1 of 3, Iss. 5

* REPLATED ON THIS DIAGRAM FOR CIRCUIT CLARITY

AB4 : Microprocessor (processor)

Fig. 16
Aug. 86 (Am. 2)

Fig. 16
Chap. 7
Page 33/34

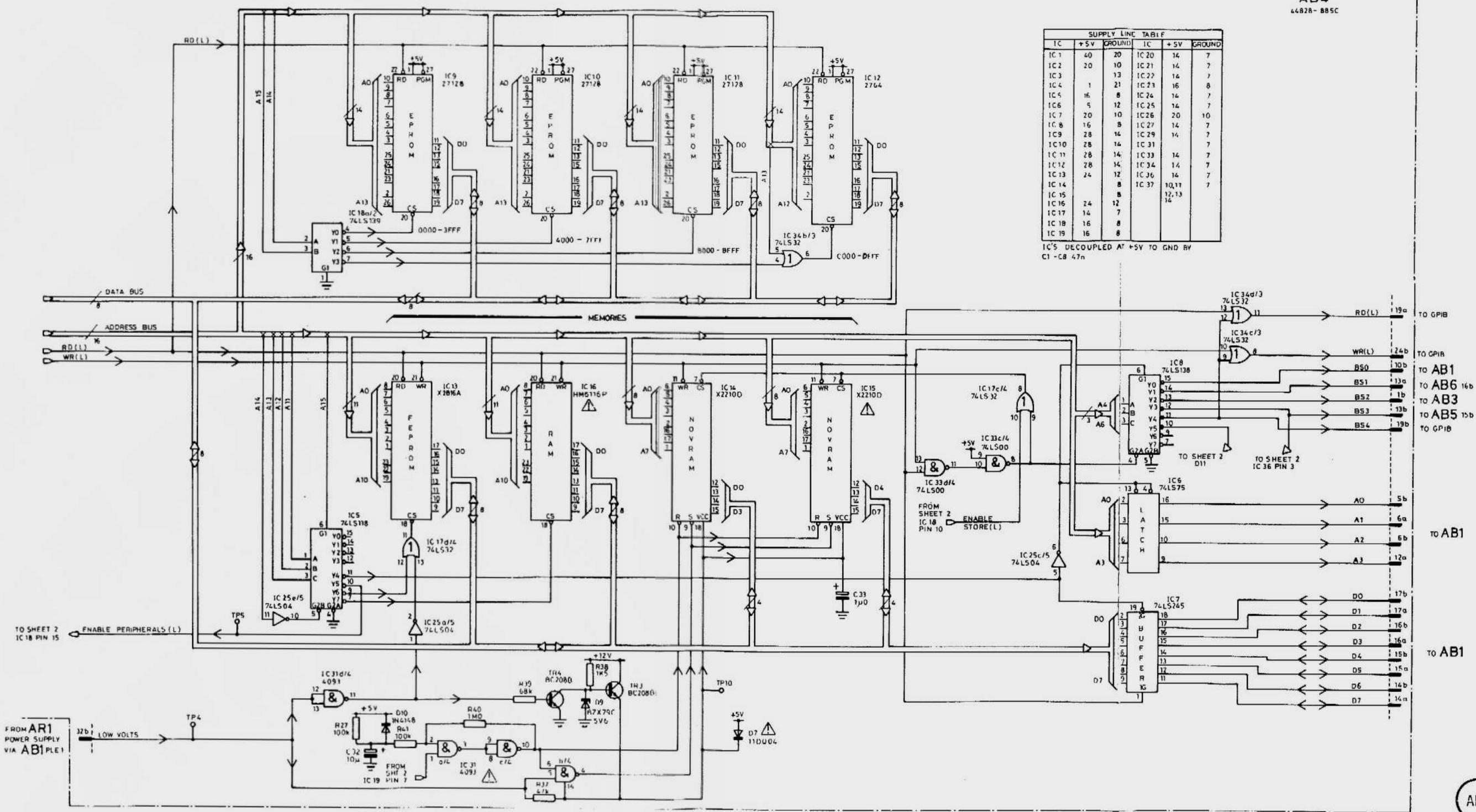
CONTINUED ON SHEET TWO

AB4

AB4
44828-885C

SUPPLY LINE TABLE					
IC	+5V	GROUND	IC	+5V	GROUND
IC 1	40	20	IC 20	14	7
IC 2	20	10	IC 21	14	7
IC 3	13	13	IC 22	14	7
IC 4	1	21	IC 23	16	8
IC 5	16	8	IC 24	14	7
IC 6	5	12	IC 25	14	7
IC 7	20	10	IC 26	20	10
IC 8	16	8	IC 27	14	7
IC 9	28	14	IC 29	14	7
IC 10	28	14	IC 31	14	7
IC 11	28	14	IC 33	14	7
IC 12	28	14	IC 34	14	7
IC 13	24	12	IC 36	14	7
IC 14	8	8	IC 37	10,11	7
IC 15	8	8		12,13	7
IC 16	24	12		14	7
IC 17	14	7			
IC 18	16	8			
IC 19	16	8			

IC'S DECOUPLED AT +5V TO GND BY
C1 - C8 47n



Drg. No. Z 44828-885C
Sh. 3 of 3, Iss. 3

AB4 : Microprocessor (memory)

Fig. 18
Mar. 86 (Am. 1)

Fig. 18
Chap. 7
Page 37

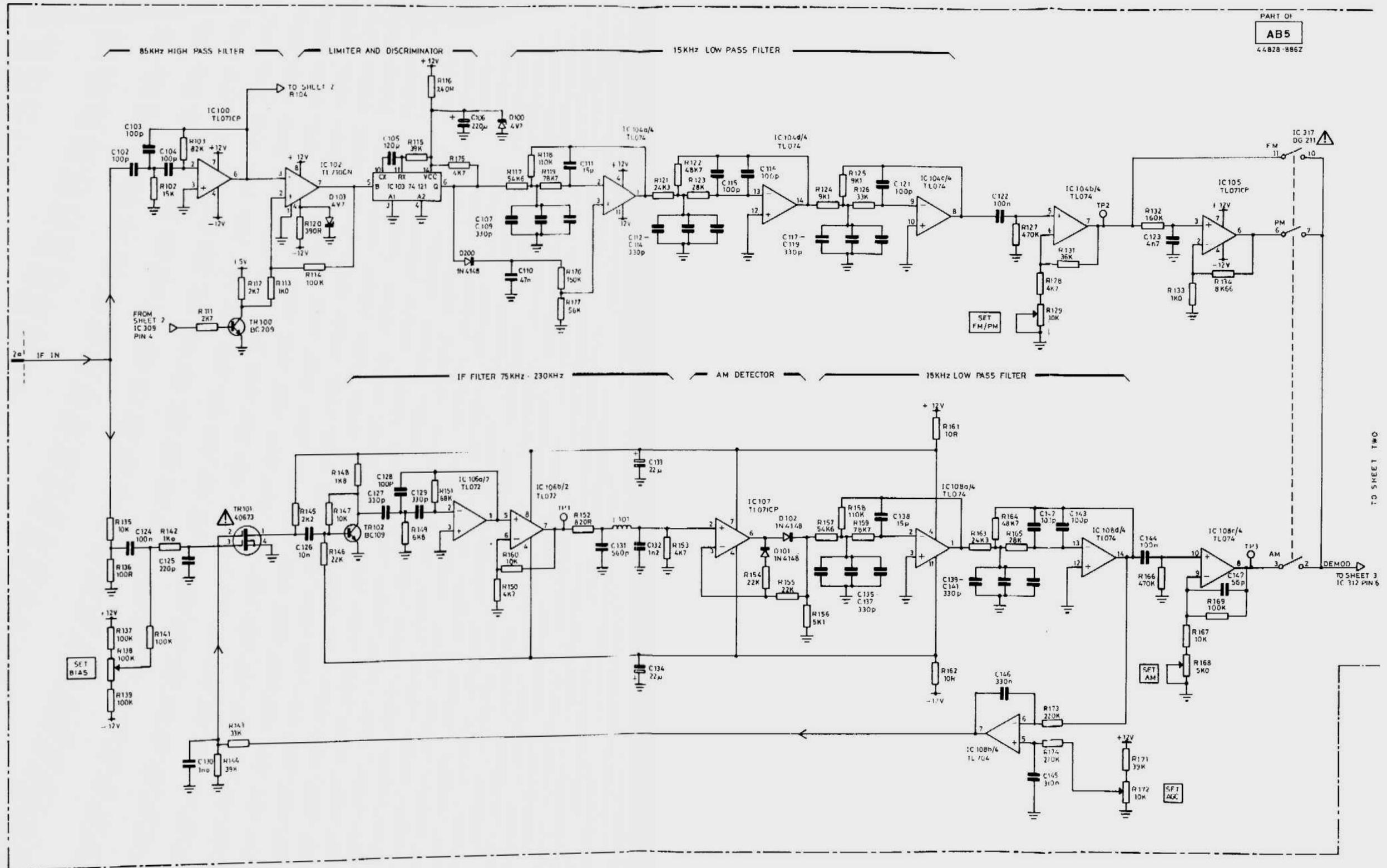
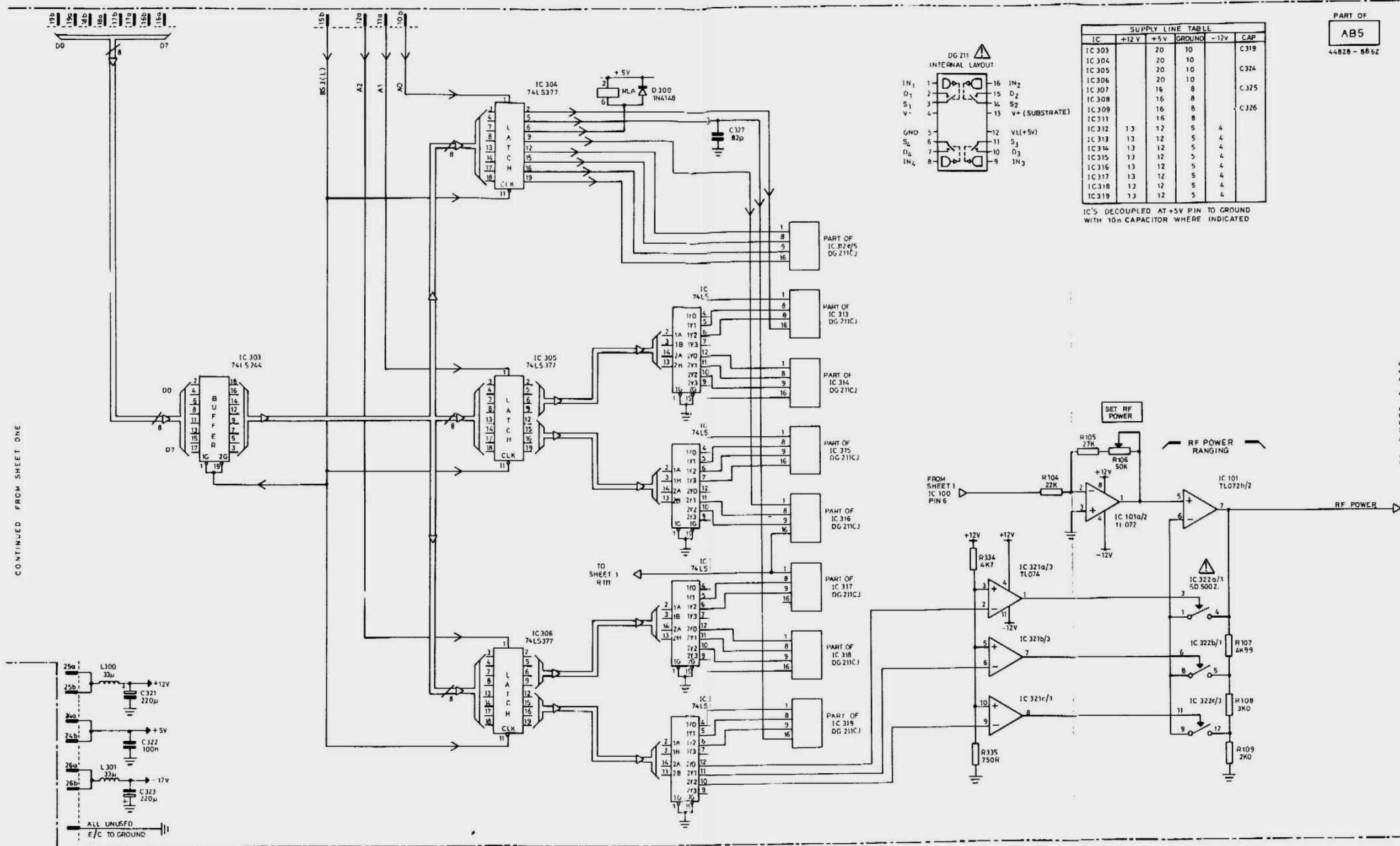


Fig. 19
Nov. 87 (Am. 5)

Drq. No. Z 44828-886Z
Sht. 1 of 3, Iss. 6

AB5 : Demodulation & scope (i.f. demodulation)

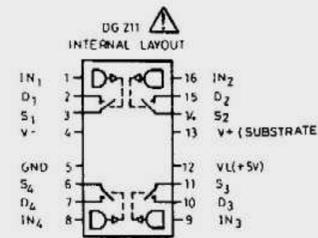
Fig. 19
Chap. 7
Page 39/40



SUPPLY LINE TABLE

IC	+12V	+5V	GROUND	-17V	CAP
IC 303			20	10	C319
IC 304			20	10	
IC 305			20	10	C324
IC 306			20	10	
IC 307			16	8	C325
IC 308			16	8	
IC 309			16	8	C326
IC 311			16	8	
IC 312	13	12	5	4	
IC 313	13	12	5	4	
IC 314	13	12	5	4	
IC 315	13	12	5	4	
IC 316	13	12	5	4	
IC 317	13	12	5	4	
IC 318	13	12	5	4	
IC 319	13	12	5	4	

IC'S DECOUPLED AT +5V PIN TO GROUND WITH 10nF CAPACITOR WHERE INDICATED



PART OF
AB5
44828-886Z

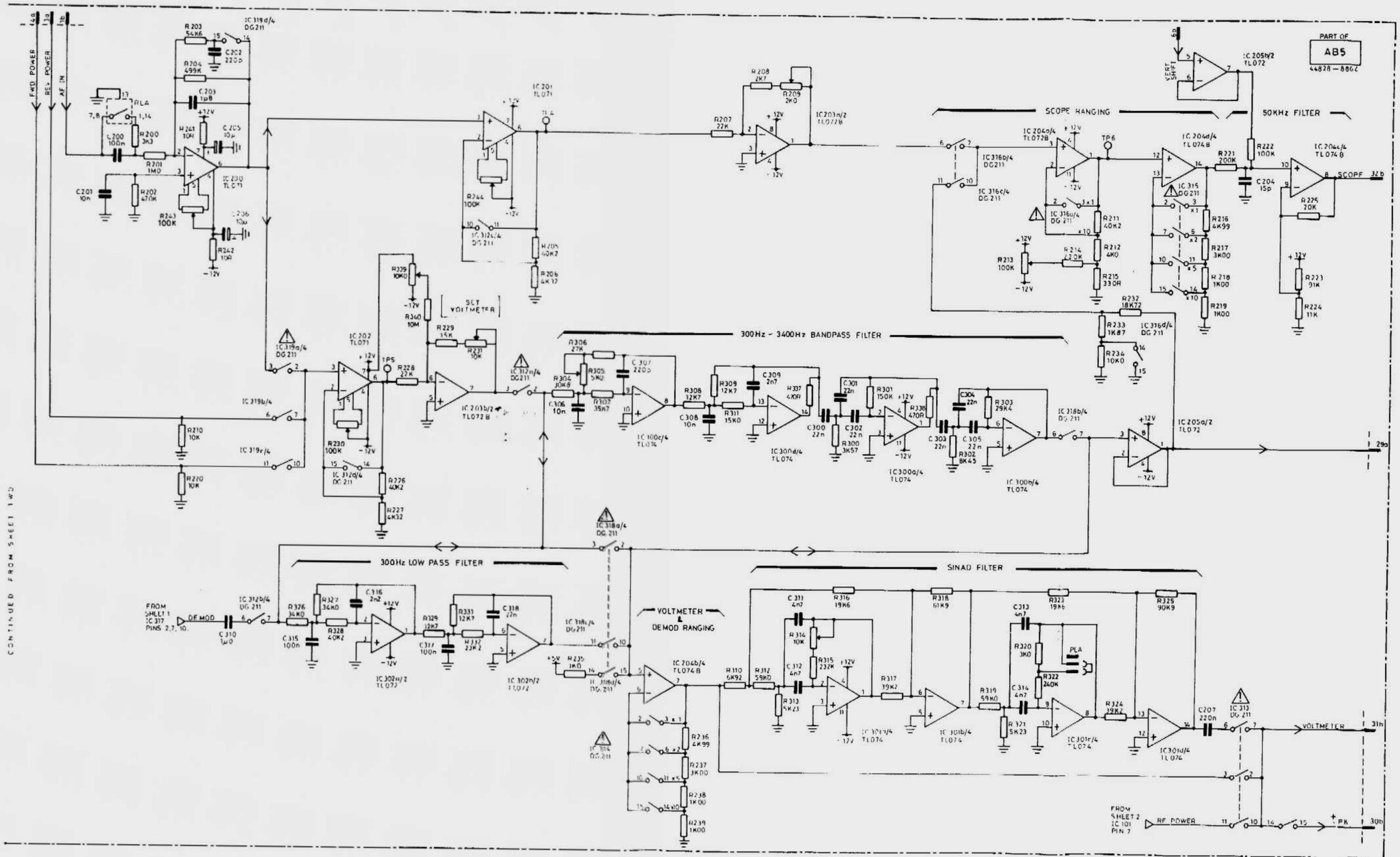
Drg. No. Z 44828-886Z
Sh. 2 of 3, Iss. 4

AB5 : Demodulation scope (switching)

Fig. 20
Mar. 86 (Am. 1)

Fig. 20
Chap. 7
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AB5



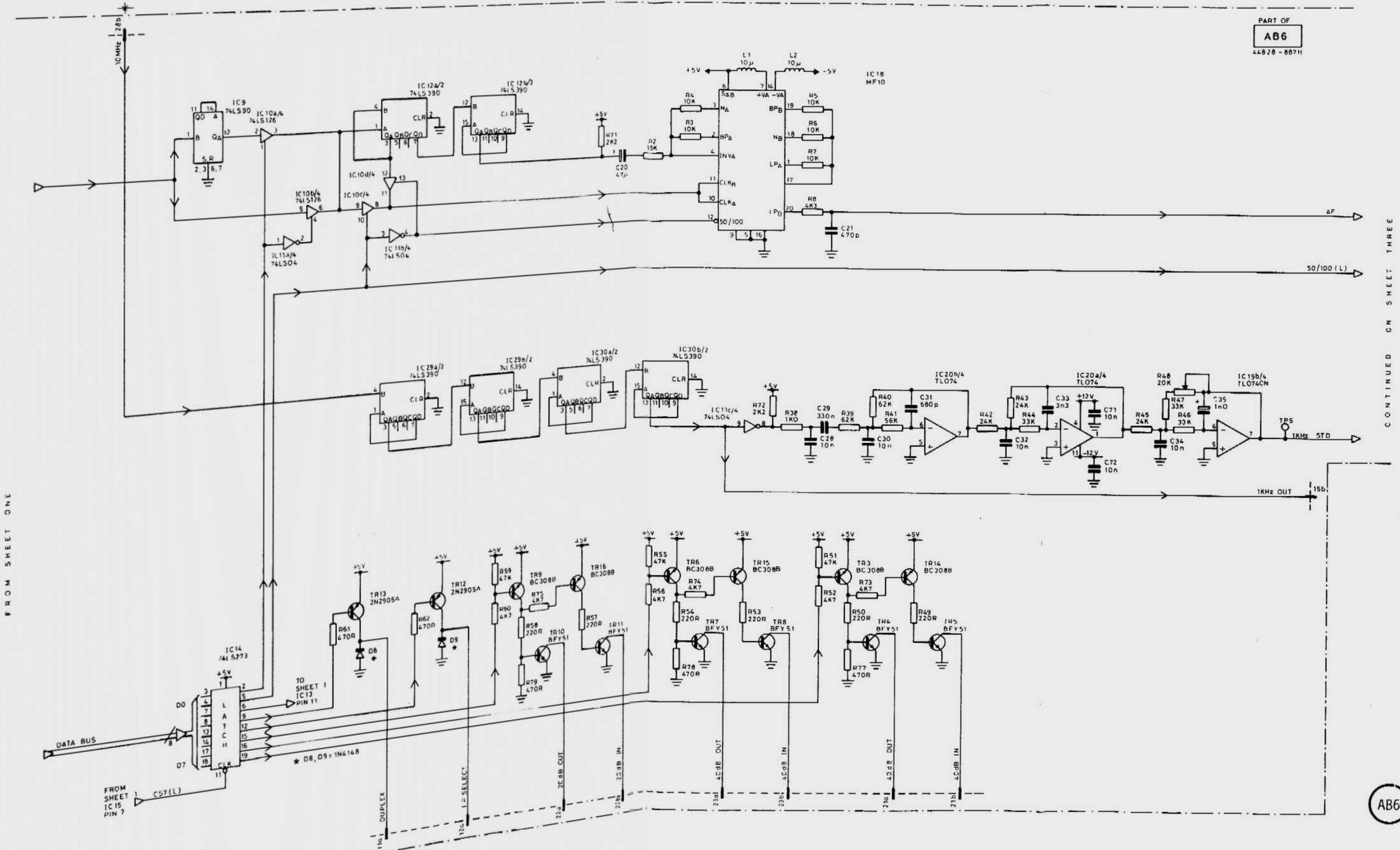
CONTINUED FROM SHEET 1W

Fig. 21
Nov. 87 (Am. 5)

Drg. No. Z 44828-886Z
Sht. 3 of 3, Iss. 8

AB5 : Demodulation & scope (ranging & filters)

PART OF
AB6
44828-887H



FROM SHEET ONE

CONTINUED ON SHEET THREE

AB6

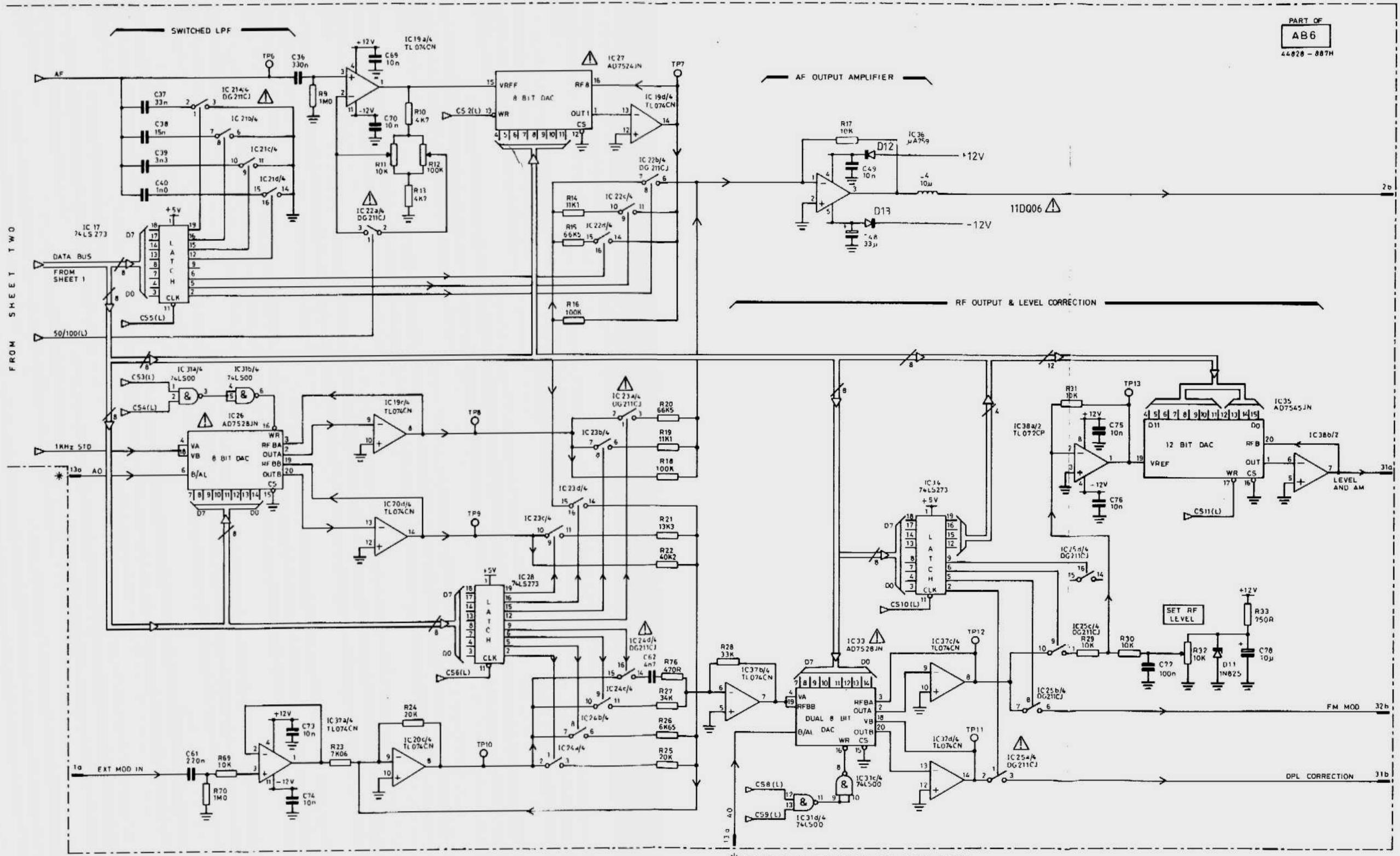
* DUPLICATED ON THIS DRAWING FOR CIRCUIT CLARITY

AB6 : AF synthesizer (filter & dividers)

Fig. 23
Nov. 87 (Am. 5)

Drg. No. Z 44828-887H
Sht. 2 of 3, Iss. 3

Fig. 23
Chap. 7
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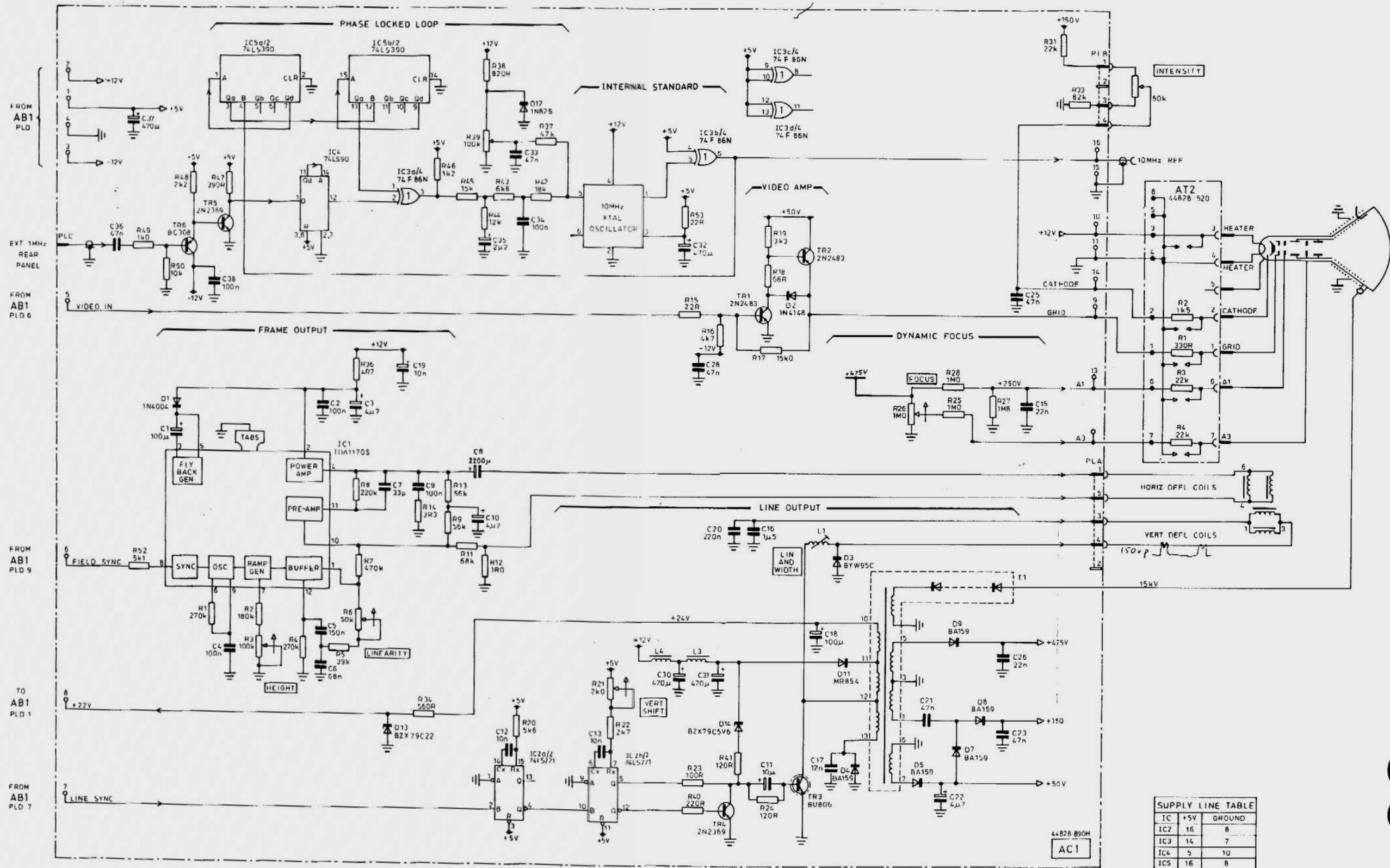
Drq. No. Z 44828-887H
Sht. 3 of 3, Iss. 5

AB6 : AF synthesizer (D-A conversion)

Fig. 24

Nov. 87 (Am. 5)

Fig. 24
Chap. 7
Page 49



SUPPLY LINE TABLE

IC	+5V	GROUND
IC2	16	8
IC3	14	7
IC4	5	10
IC5	16	8

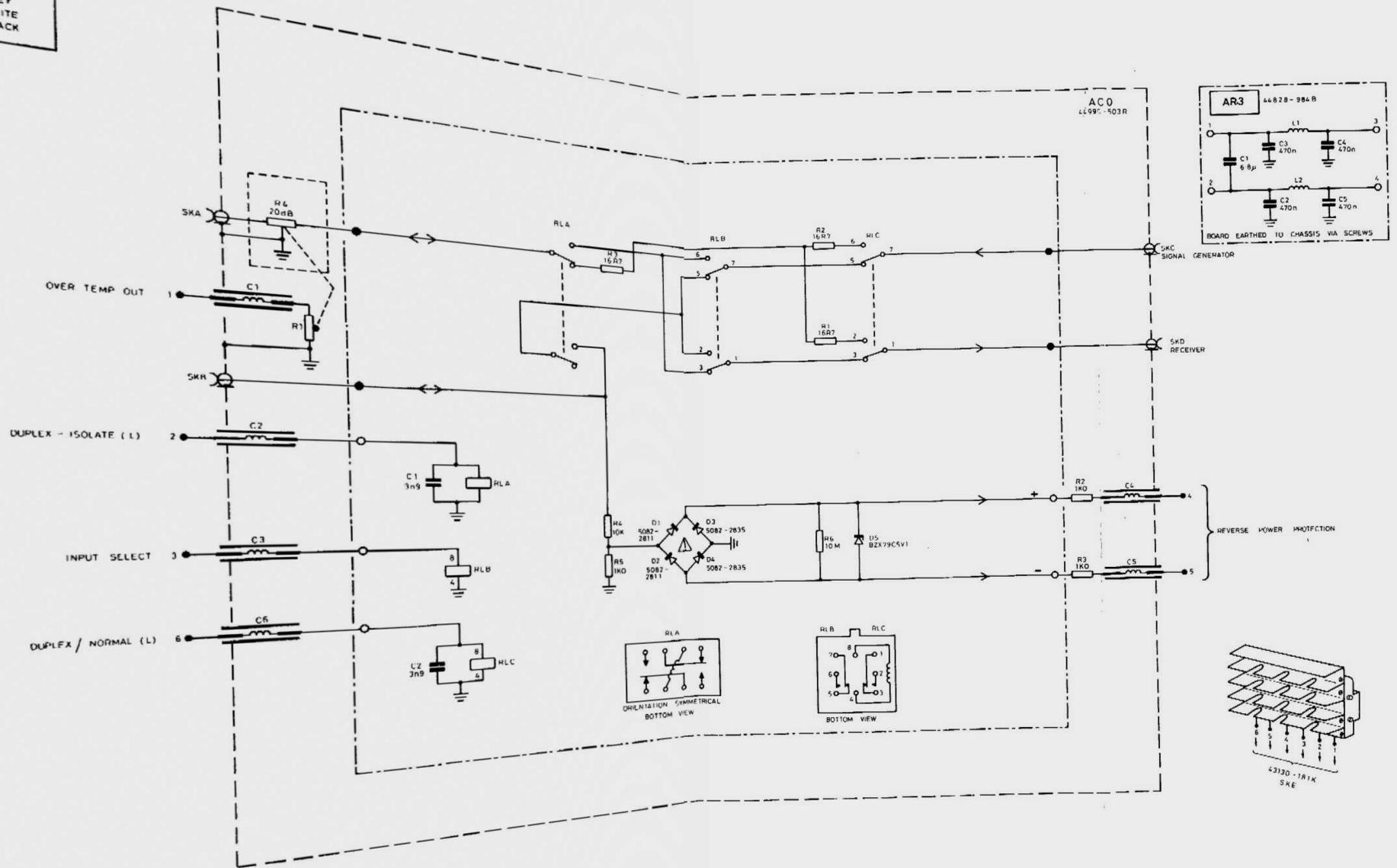
Drg. No. Z 44828-890H
Sh. 1 of 1, Iss. 5

Fig. 25
Nov. 87 (Am. 5)

AC1 : CRT drive
AT2 : CRT base

Fig. 25
Chap. 7
Page 51

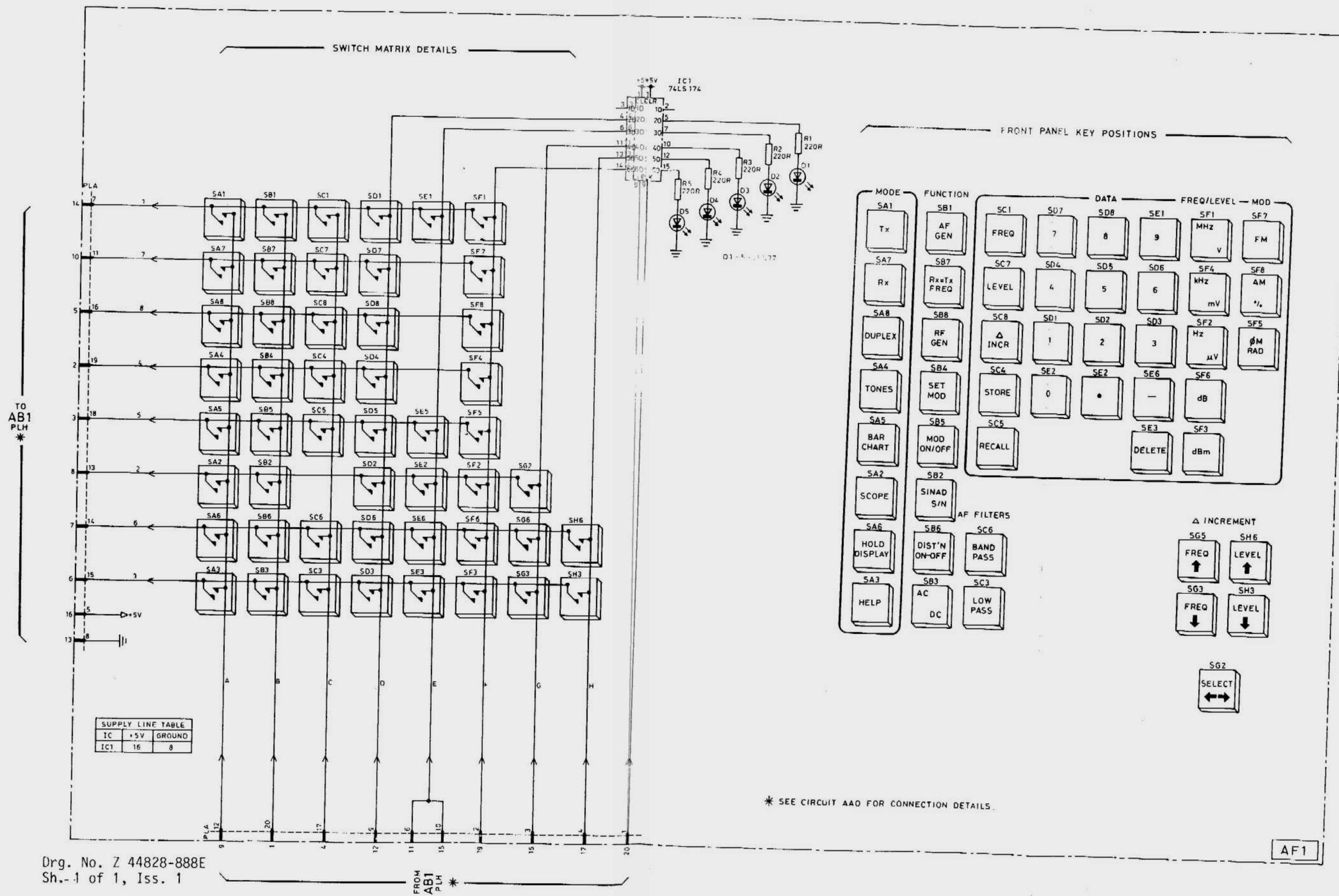
AC2	AB1 PLM	COLOUR
3	1	BROWN
1	2	RED
6	3	ORANGE
5	4	YELLOW
NC	5	GREEN
NC	6	BLUE
4	7	MAUVE
2	8	GREY
NC	9	WHITE
	KEY	BLACK



Drg. No. Z 44828-876X
Sh. 1 of 1, Iss. 2

AC2 : Input switching
AR3 : DC filter

Fig. 26
Nov. 87 (Am. 5)

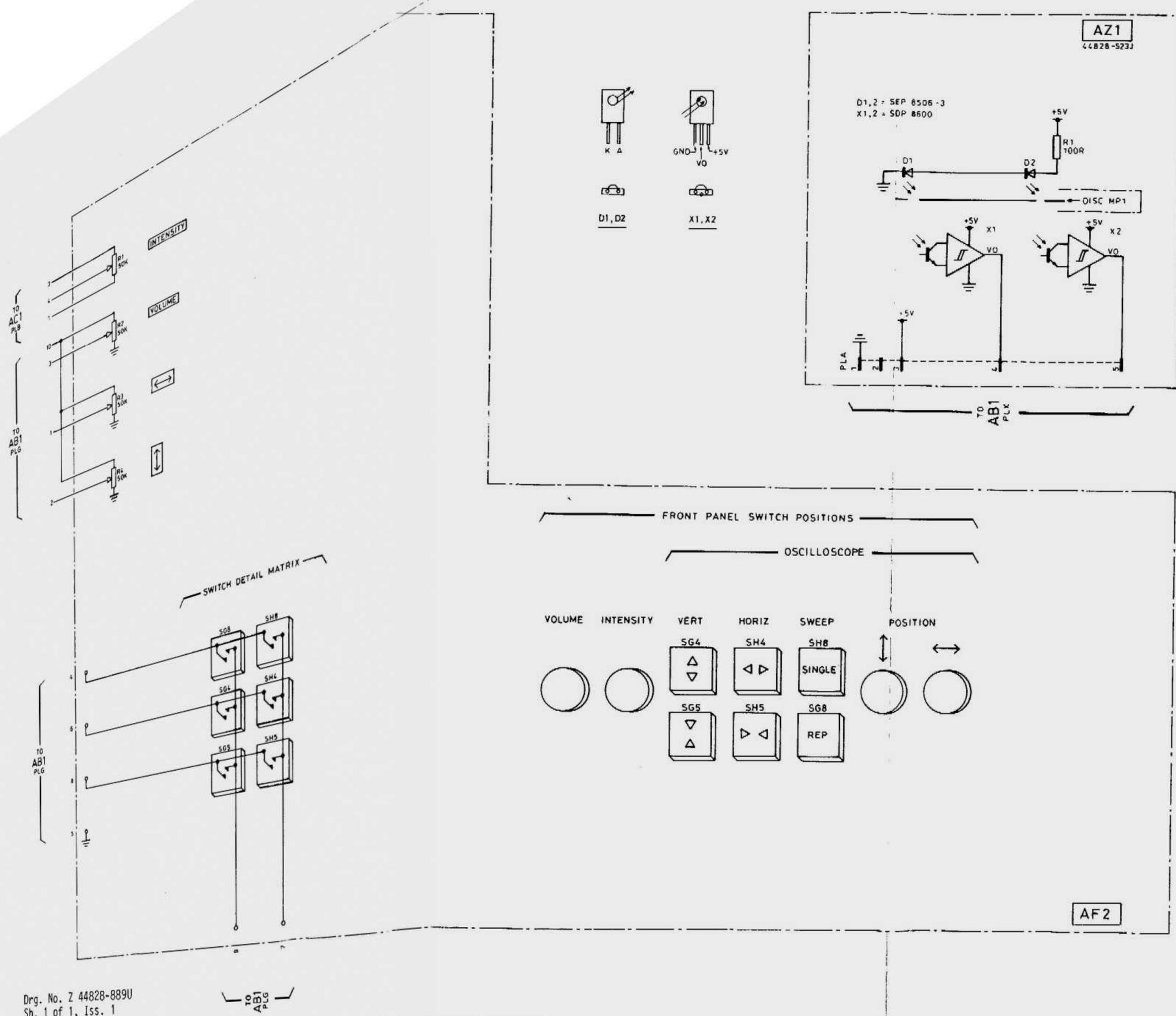


Drg. No. Z 44828-888E
Sh.-1 of 1, Iss. 1

Fig. 27
June 85

AF1 : Main keyboard

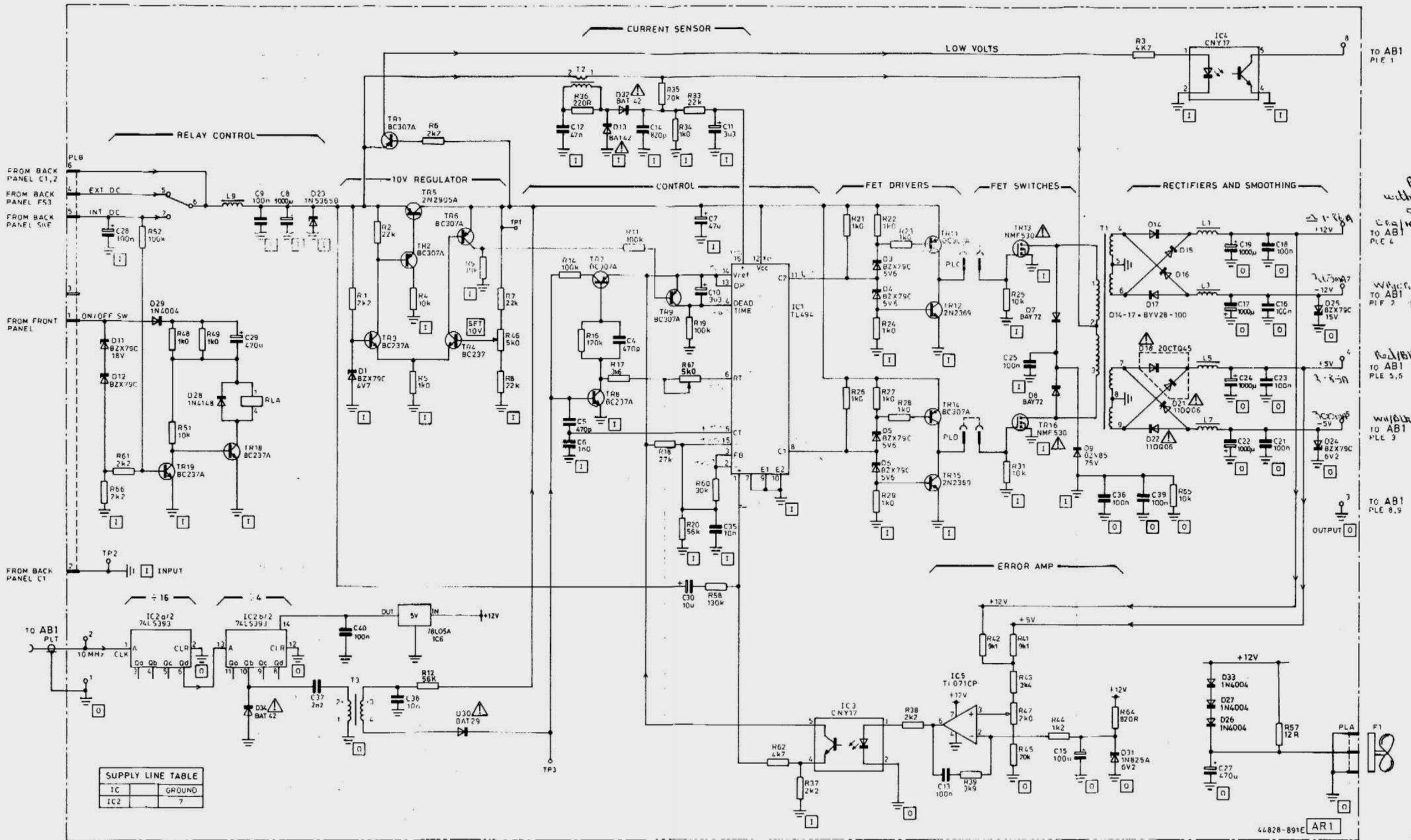
Fig. 27
Chap. 7
Page 55



AZ1
AF2

Drg. No. 2 44828-889U
Sh. 1 of 1, Iss. 1

AF2 : Scope keyboard
AZ1 : Optical encoder



Resistance with Fluke 17 on 2
TO AB1 PLE 4
Wiper TO AB1 PLE 7
Red/Blk TO AB1 PLE 5.5
White TO AB1 PLE 3

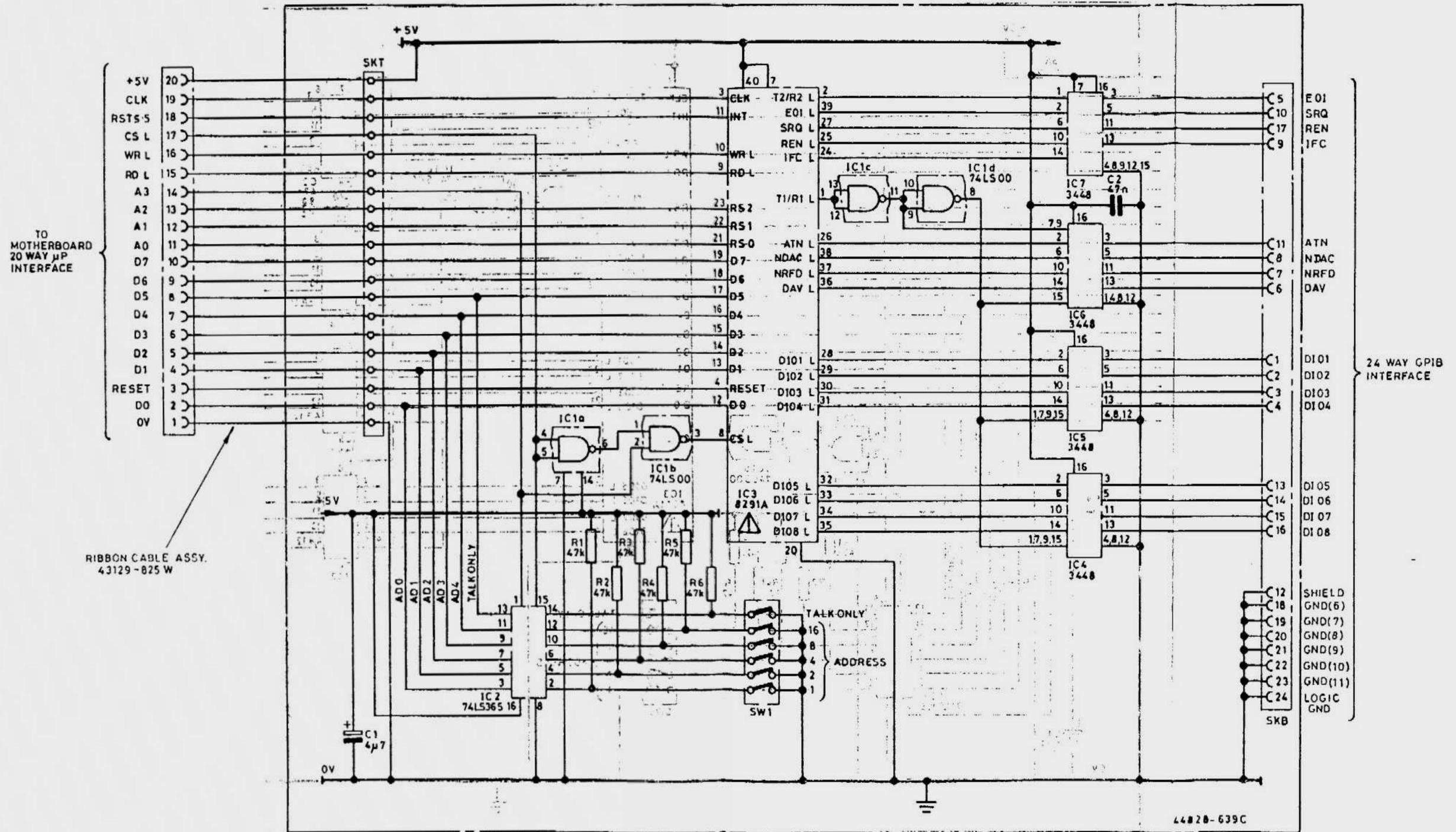
Drg. No. Z 44828-891E
Sht. 1 of 1, Iss. 9

Fig. 29
Nov. 87 (Am. 5)

AR1 : Power supply

Fig. 29
Chap. 7
Page 59

COMPONENT MARKED \triangle IS STATIC SENSITIVE, PRECAUTIONS AS PER MIC 2370
IC2 ADDRESS = CS L = A3
IC3 ADDRESS = CS L = A3



RIBBON CABLE ASSY.
43129-825 W

Drg. No. Z 54433-002Y
Sh. 1 of 1, Iss. 1

Fig. 30
June 85

AGD X GPIB unit

Fig.
Chap.
Page 61/